

MAR 28 2006

To: Technology Center 2800

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Alfred Haeusler et al. Art Unit 2812

Serial No.: 10/824,745 Examiner: Anya, Igwe U

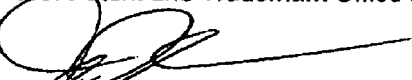
Filed: 04/15/2004 Docket No.: TID-34858

Customer No.: 23494 Conf. No.: 7054

For: METHOD OF FABRICATING AN INTEGRATED SILICON-GERMANIUM
HETEROBIPOLAR TRANSISTOR AND AN INTEGRATED SILICON-GERMANIUM
HETEROBIPOLAR TRANSISTOR

CERTIFICATION OF FACSIMILE TRANSMISSION

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FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT (7 Pages)
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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Haeusler	
TITLE OF INVENTION: Method of Fabricating . . .	
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